

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination IINO ET AL.	
		Examiner Matthew A. Anderson	Art Unit 1765	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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X	C	US-6,461,582 B2	10-2002	Weber et al.	423/328.2
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, Volume 1: Process Technology, Sunset Press, CA, USA, pp. 59-61, 1986.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Matthew Anderson 4/13/2005